

GOA UNIVERSITY

RESULT REGISTER FOR M.E. IN ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN MAY, 2014
 COLLEGE : GOA COLLEGE OF ENGINEERING, FARMAGUDI, GOA.

Seat No.	P R No.	Name of the Candidate	Advanced Digital Signal Processing		ASIC Design & FPGA				Memory Design & Testing				Embedded Systems				Testing & Fault Tolerance				Digital Signal Processing & Embedded Systems - LAB				Designing with FPGAs - LAB		SGPA Sem. II	Sem II P/F	SGPA Sem. I	Sem I P/F	CGPA	Remarks
			MEMIC 2.1		MEMIC 2.2				MEMIC 2.3				MEMIC 2.4				MEMIC 2.5				MEMIC 2.6				MEMIC 2.7							
			Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Theory	Internal	Practical	Internal	Practical	Internal	Practical										
1	200707462	NAIK VISHAL RAMESH	2	CC + 6 CC +	2	BC + 6	BC	P	2	BC + 6	FF	F	2	BB + 6	FF	F	2	BC + 6	CC	P	2	BC + 2	BC +	2	BC + 2	BB +	04.17	F	05.42	P	04.80	FAILS
2	200703880	SHEETI KARWARKAR JAYESH SUBHASH	2	CC + 6 BC P	2	AB + 6	CC	P	2	BB + 6	CC	+	2	AA + 6	CC	+	2	AB + 6	BC	+	2	AB + 2	BC +	2	AB + 2	BB +	06.13	P	06.08	P	06.11	PASSES

Grade	Grade Point	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By *[Signature]*
 Checked By *[Signature]*
 Date 25/7/2014



[Signature]
 25/7/2014
 S. S. J. Figueiredo
 Asslt. Registrar-Exam(Professional)

[Signature]
 25/7/14
 Donald A. E. Rodrigues
 Controller of Examinations

[Signature]
 25.7.14
 Prof. V. P. Kamat
 Registrar

ABS - Absent; + - Grade Carried Over
 Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college
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1581
09/09/15
5/5
9/9/15

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COURSE

NC-2013

Seat No.	P R No.	Name of the Candidate	ASIC Design & FPGA			Digital Signal Processor & Embedded Systems			Design for Testability & E Waste Management			Processor Architecture & Parallel Processing			Memory Design			Parallel Processing - LAB			FPGA & Embedded Systems - LAB			SGPA Sem. II	Sem II P/F	Remarks															
			MEM 2.1			MEM 2.2			MEM 3.3			MEM 2.4			MEM 2.5			MEM 2.6			MEM 2.7																				
			Internal	Theory		Internal	Theory		Internal	Theory		Internal	Theory		Internal	Theory		Internal	Practical		Internal	Practical																			
Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F															
6	201301061	KAMAT DHAKANKAR VEERA MAHABLESHWAR	2	AB	P	4	BC	P	2	BC	P	4	BC	P	2	AB	P	4	BB	P	2	AA	P	4	BC	P	2	AB	P	2	BB	P	2	AA	P	2	BC	P	07.00	P	PASSES
7	200606414	KSHAMA RAMKRISHNA VAZE	2	AA	P	4	CC	P	2	CC	P	4	BC	P	2	BB	P	4	CC	P	2	BB	P	4	BC	P	2	BC	P	2	BB	P	2	BB	P	2	BB	P	06.26	P	PASSES
10	200907815	NAIK SONIA AJIT	2	AB	P	4	CC	P	2	CC	P	4	BC	P	2	BB	P	4	CC	P	2	CC	P	4	BC	P	2	BB	P	2	BB	P	2	BB	P	2	BB	P	06.00	P	PASSES
11	200306972	NETRAVALKAR NILANGI UMESH	2	AA	P	4	AB	P	2	AB	P	4	BC	P	2	AA	P	4	BC	P	2	AA	P	4	BB	P	2	AO	P	2	BB	P	2	AO	P	2	BB	P	07.74	P	PASSES

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AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail



Read By *[Signature]*

Checked By *[Signature]*

Date 03/09/15

[Signature]
03/9/2015
S. S. J. Figueiredo
Asstt. Registrar-Exam(Professional)

[Signature]
03/9/15
Leo V. Macedo
Controller of Examinations

[Signature]
03.9.15
Prof. V. P. Kamat
Registrar

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NS/NS/HEICD
[Signature]

1582
9/9/15

5/6
9/9/15

GOA UNIVERSITY

RESULT REGISTER FOR M.E. IN ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN NOVEMBER, 2014

COLLEGE : GOA COLLEGE OF ENGINEERING, FARMAGUDI, GOA.

COURSE NC-2013

Seat No.	P R No.	Name of the Candidate	ASIC Design & FPGA			Digital Signal Processor & Embedded Systems			Design for Testability & Waste Management			Processor Architecture & Parallel Processing			Memory Design			Parallel Processing - LAB			FPGA & Embedded Systems - LAB			SGPA Sem. II	Sem II P/F	Remarks															
			MEM 2.1			MEM 2.2			MEM 3.3			MEM 2.4			MEM 2.5			MEM 2.6			MEM 2.7																				
			Internal	Theory		Internal	Theory		Internal	Theory		Internal	Theory		Internal	Theory		Internal	Practical		Internal	Practical																			
Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F	Credit	Grade	P/F															
2	200806127	KUDALKAR SHREYA S.	2	AB	+	4	BB	+	2	CC	+	4	BC	+	2	CC	+	4	CC	P	2	CC	+	4	BC	+	2	CC	+	2	CC	+	2	BB	+	2	CC	+	05.79	P	PASSES

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AO	10	Outstanding
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Read By pphd6

Checked By 40

Date 03/09/2015

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S. S. J. Figueiredo
 03/09/2015
 S. S. J. Figueiredo
 Asstt. Registrar-Exam(Professional)

Leo V. Macedo
 03/09/15
 Leo V. Macedo
 Controller of Examinations

Prof. V. P. Kamat
 03-9-15
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 Registrar



NB/WB/HEFED
 ✓